

General Description

The MY16N65F is silicon N-channel Enhanced VDMOSFETS, obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy.

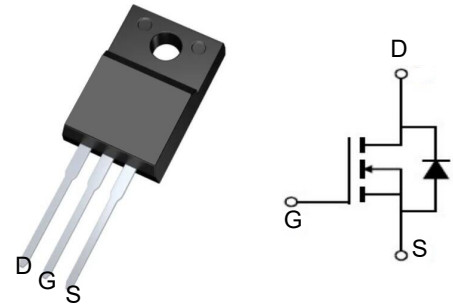


Features

V_{DSS}	650	V
I_D	16	A
P_D ($T_C = 25^\circ\text{C}$)	62	W
$R_{DS(ON)}$ (at $V_{GS} = 10\text{V}$)	0.52	Ω

Application

- High efficiency switch mode power supplies
- Power factor correction
- Electronic lamp ballast



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY16N65F	TO-220F	MY16N65F	1000

Absolute Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameters	Ratings	Unit
V_{DSS}	Drain-Source Voltage	650	V
V_{GS}	Gate-Source Voltage-Continuous	± 30	V
I_D	Drain Current-Continuous (Note 2)	16	A
I_{DM}	Drain Current-Single Plused (Note 1)	64	A
P_D	Power Dissipation (Note 2)	62	W
T_j	Max.Operating junction temperature	150	$^\circ\text{C/W}$

Electrical Characteristics ($T_c=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameters	Min	Typ	Max	Units	Conditions
Static Characteristics						
B _{VDS}	Drain-Source Breakdown VoltageCurrent (Note 1)	650	--	--	mA	I _D =250μA V _{GS} =0V , T _J =25°C
V _{GS(th)}	Gate Threshold Voltage	2.0	--	4.0	V	V _{DS} =V _{GS} , I _D =250μA
R _{DS(on)}	Drain-Source On-Resistance	--	0.52	--	Ω	V _{GS} =10V , I _D =8A
I _{GSS}	Gate-Body Leakage Current	--	--	±90	nA	V _{GS} =±30V , V _{DS} =0
I _{DSS}	Zero Gate Voltage Drain Current	--	--	200	nA	V _{DS} =650V , V _{GS} =0
g _{fs}	Forward Transconductance	--	6.0	--	S	V _{DS} =30V, I _D =8A
Switching Characteristics						
T _{d (on)}	Turn-On Delay Time	--	16	--	ns	V _{DS} =325V , I _D =16A, R _G =25Ω (Note 2)
T _r	Rise Time	--	45	--	ns	
T _{d (off)}	Turn-Off Delay Time	--	85	--	ns	
T _f	Fall Time	--	18	--	ns	
Q _g	Total Gate Charge	--	55	--	nC	V _{DS} =520V, V _{GS} =10V I _D =16A (Note 2)
Q _{gs}	Gate-Source Charge	--	15	--	nC	
Q _{gd}	Gate-Drain Charge	--	21	--	nC	
Dynamic Characteristics						
C _{iss}	Input Capacitance	--	2800	--	pF	V _{DS} =25V , V _{GS} =0, f=1MHz
C _{oss}	Output Capacitance	--	380	--	pF	
C _{rss}	Reverse Transfer Capacitance	--	20	--	pF	
I _S	Continuous Drain-Source Diode ForwardCurrent (Note 2)	--	--	16	A	
V _{SD}	Diode Forward On-Voltage	--	--	0.9	V	I _S =16A , V _{GS} =0
R _{th(j-c)}	Thermal Resistance, Junction to Case	--	--	2.02	°C/W	

Note 1: Repetitive Rating : Pulse width limited by maximum junction temperature

Note 2: Pulse test: $PW \leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Ratings and Characteristic curves

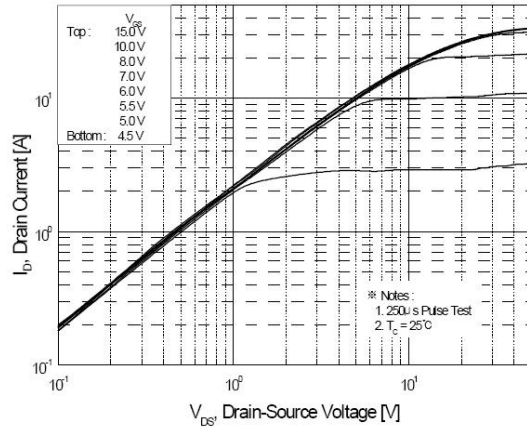


Figure 1. On-Region Characteristics

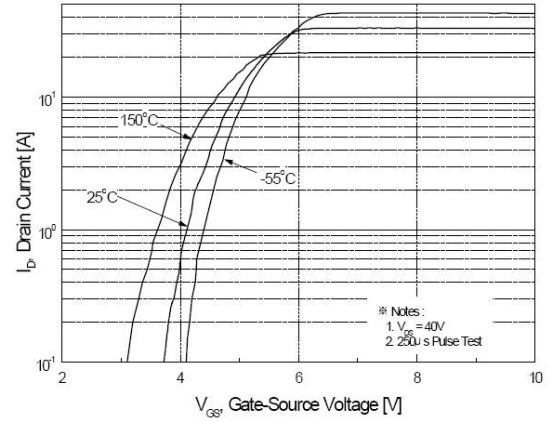


Figure 2. Transfer Characteristics

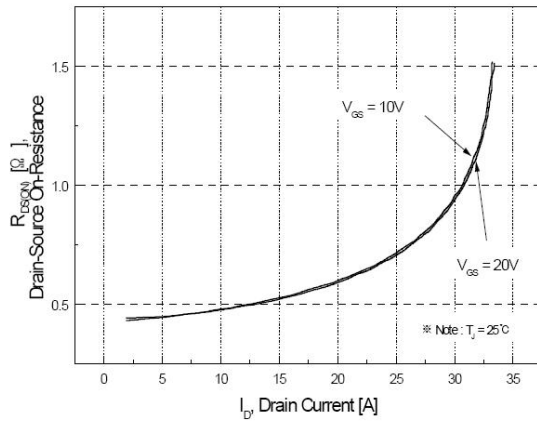


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

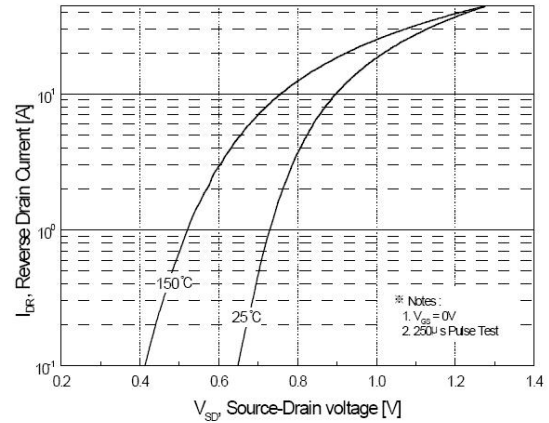


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

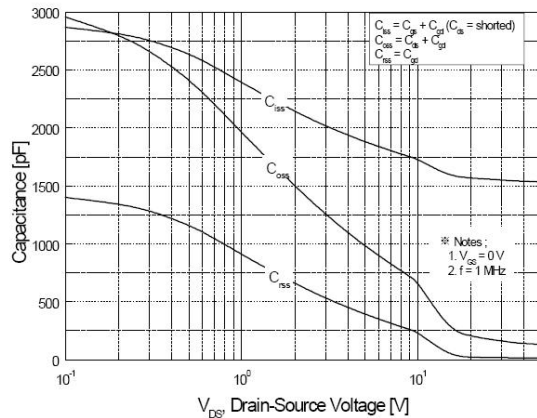


Figure 5. Capacitance Characteristics

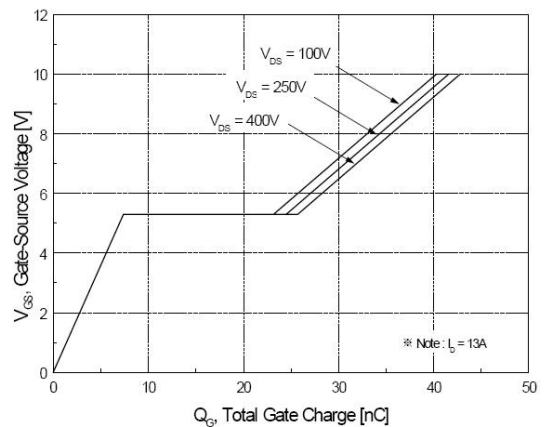


Figure 6. Gate Charge Characteristics

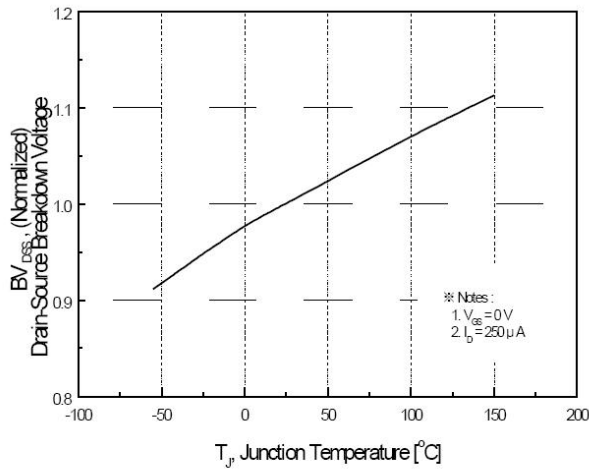


Figure 7. Breakdown Voltage Variation vs Temperature

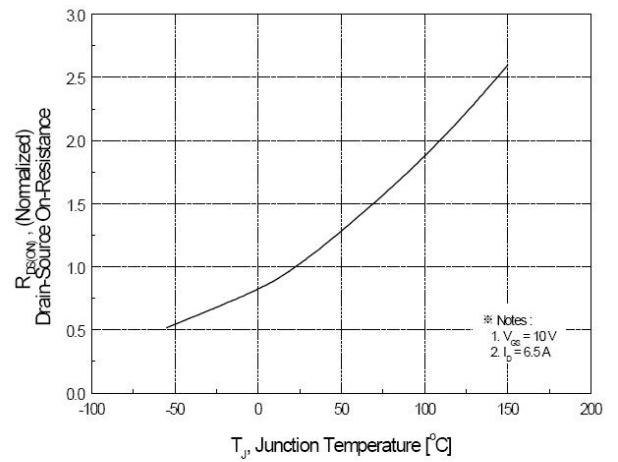


Figure 8. On-Resistance Variation vs Temperature

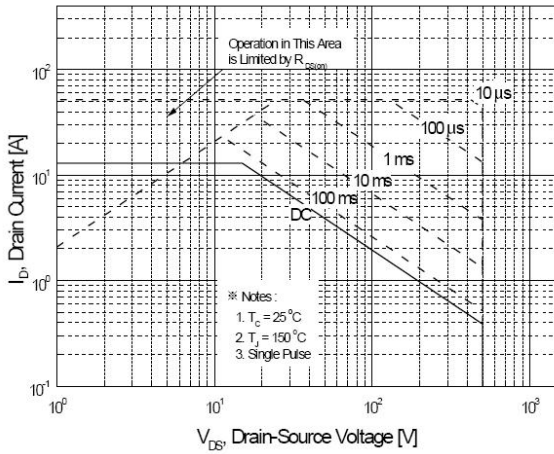


Figure 9. Maximum Safe Operating Area

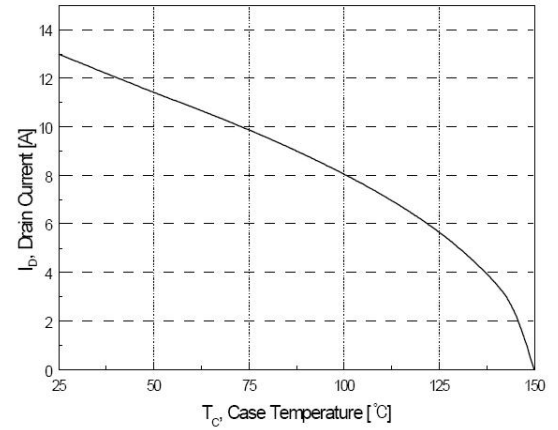


Figure 10. Maximum Drain Current vs Case Temperature

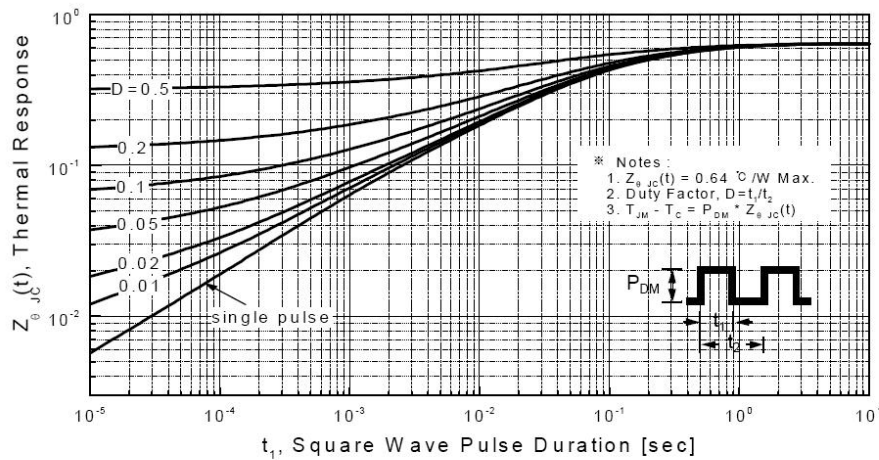
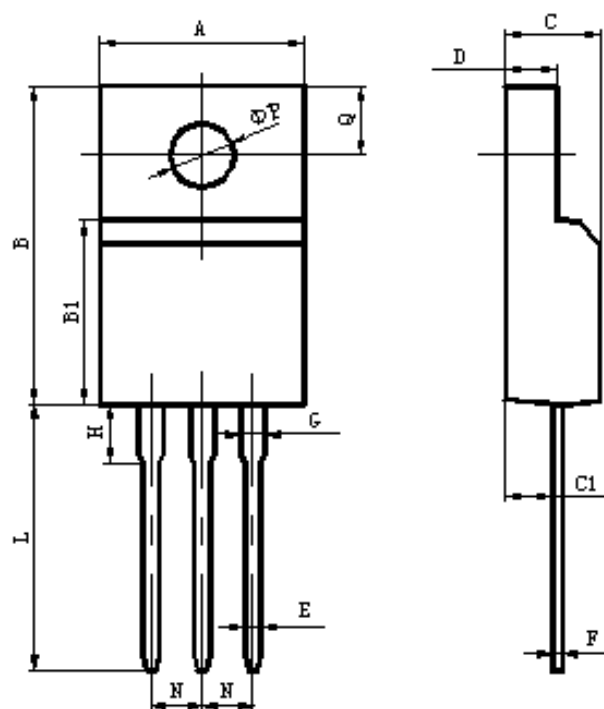


Figure 11. Transient Thermal Response Curve

Package Mechanical Data-TO-220F Single



Items	Values(mm)	
	MIN	MAX
A	9.60	10.4
B	15.4	16.2
B1	8.90	9.50
C	4.30	4.90
C1	2.10	3.00
D	2.40	3.00
E	0.60	1.00
F	0.30	0.60
G	1.12	1.42
H	3.40	3.80
	2.40	2.90
L*	12.0	14.0
N	2.34	2.74
Q	3.15	3.55
Φ P	2.90	3.30